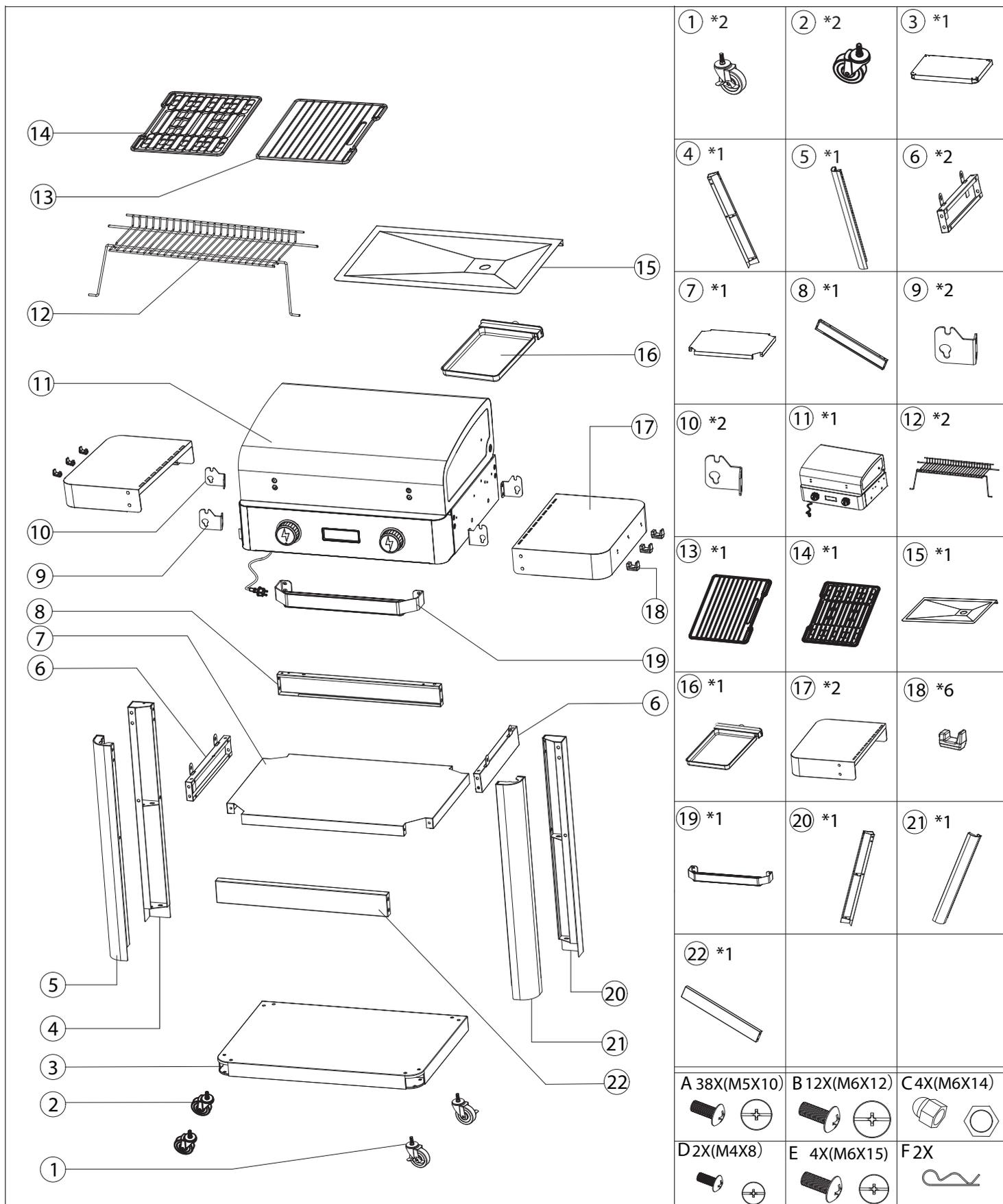
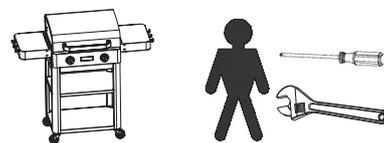


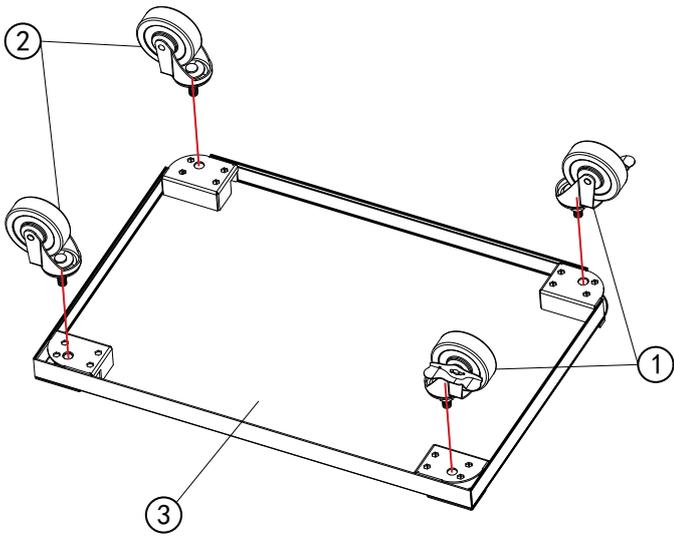
# Instructions

## DLL1S1M1T1J1 AM

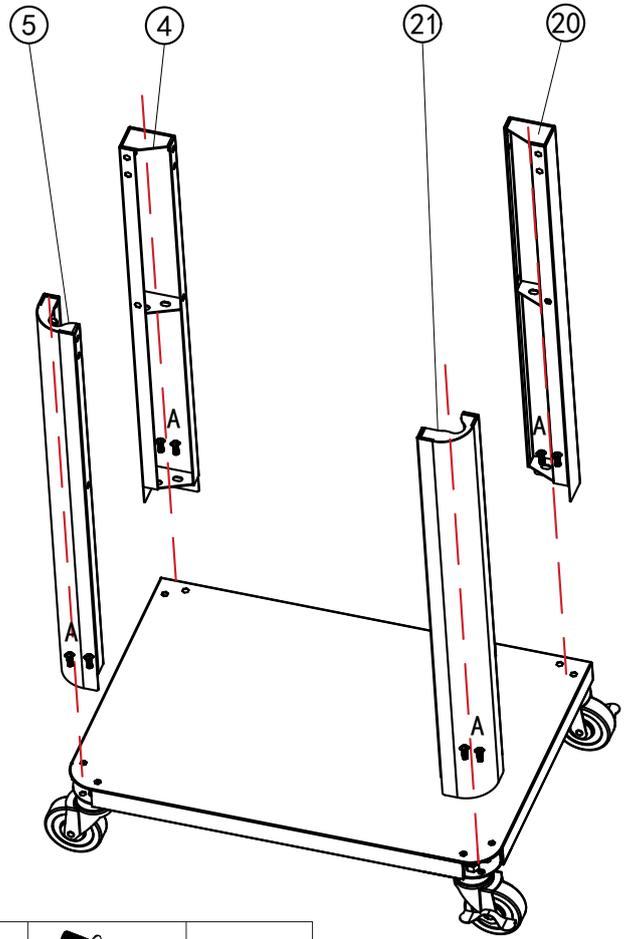


① *2 	② *2 	③ *1 
④ *1 	⑤ *1 	⑥ *2 
⑦ *1 	⑧ *1 	⑨ *2 
⑩ *2 	⑪ *1 	⑫ *2 
⑬ *1 	⑭ *1 	⑮ *1 
⑯ *1 	⑰ *2 	⑱ *6 
⑲ *1 	⑳ *1 	㉑ *1 
㉒ *1 		
A 38X(M5X10) 	B 12X(M6X12) 	C 4X(M6X14) 
D 2X(M4X8) 	E 4X(M6X15) 	F 2X 

1

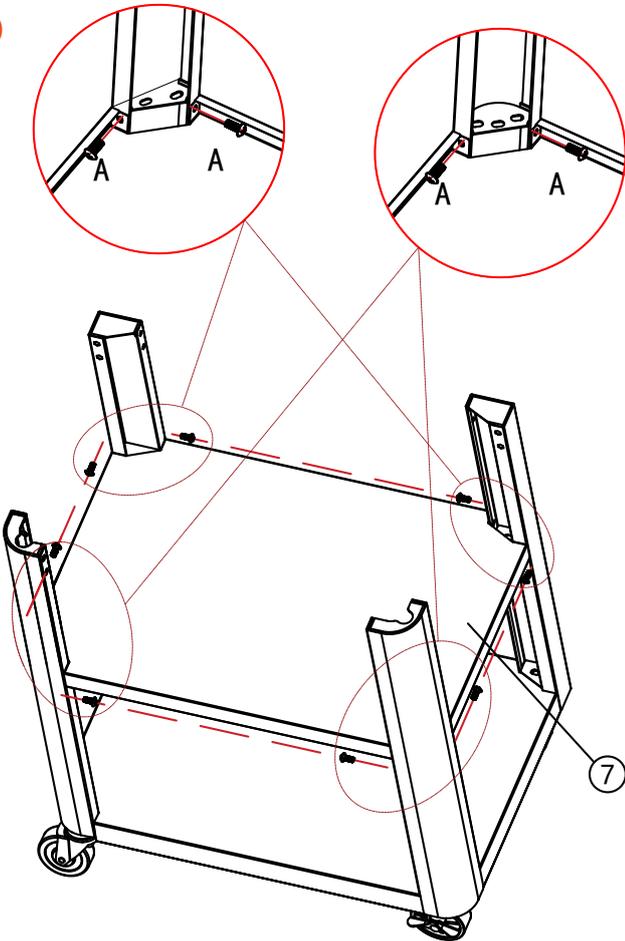


2



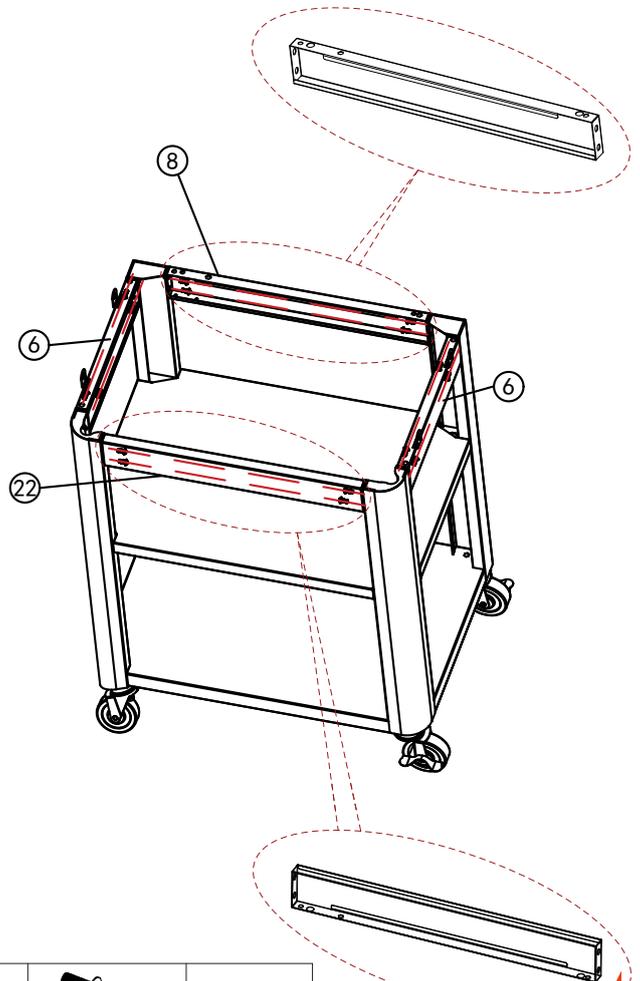
A		8 PCS
---	---	-------

3



A		8 PCS
---	---	-------

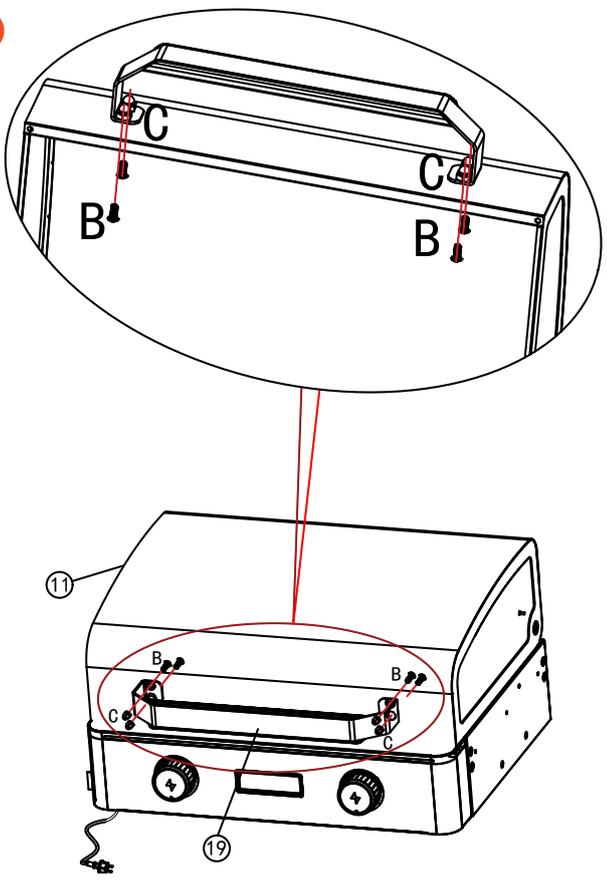
4



A		16 PCS
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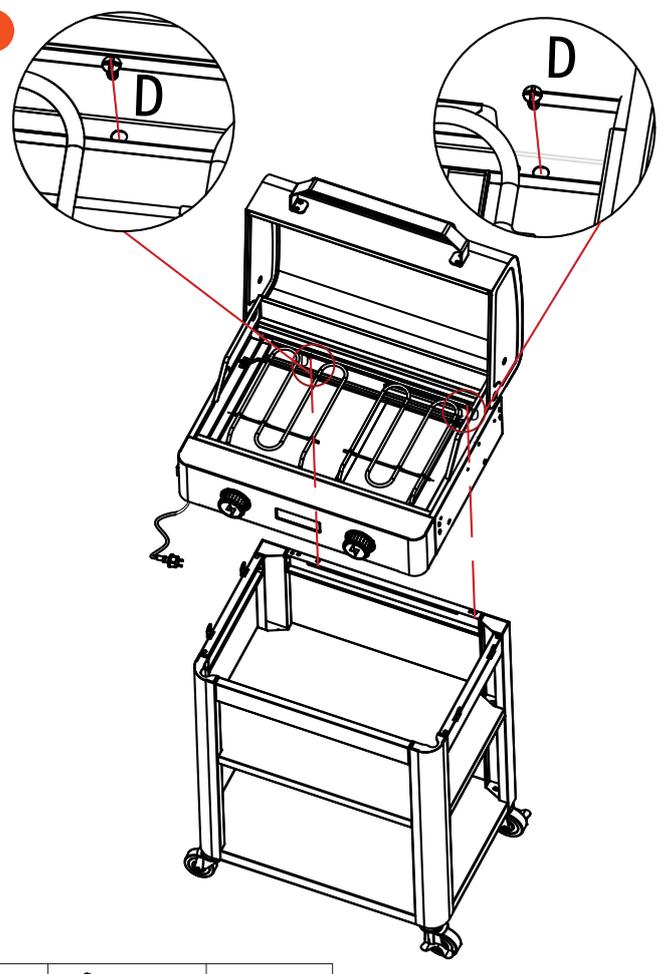


5



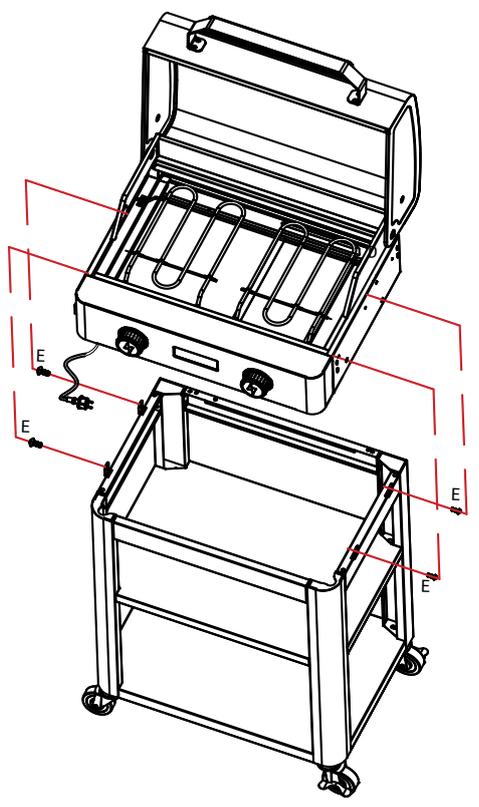
B		4 PCS
C		4 PCS

6



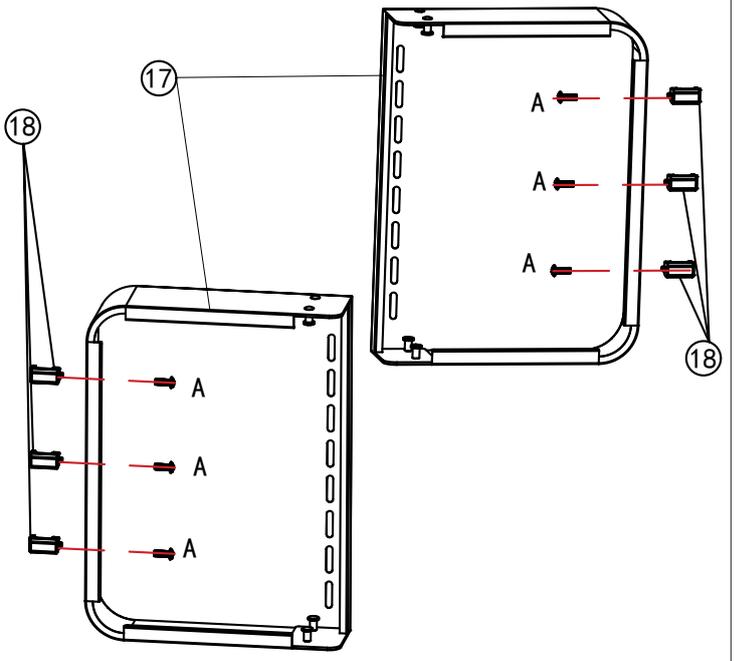
D		2 PCS
---	--	-------

7



E		4 PCS
---	--	-------

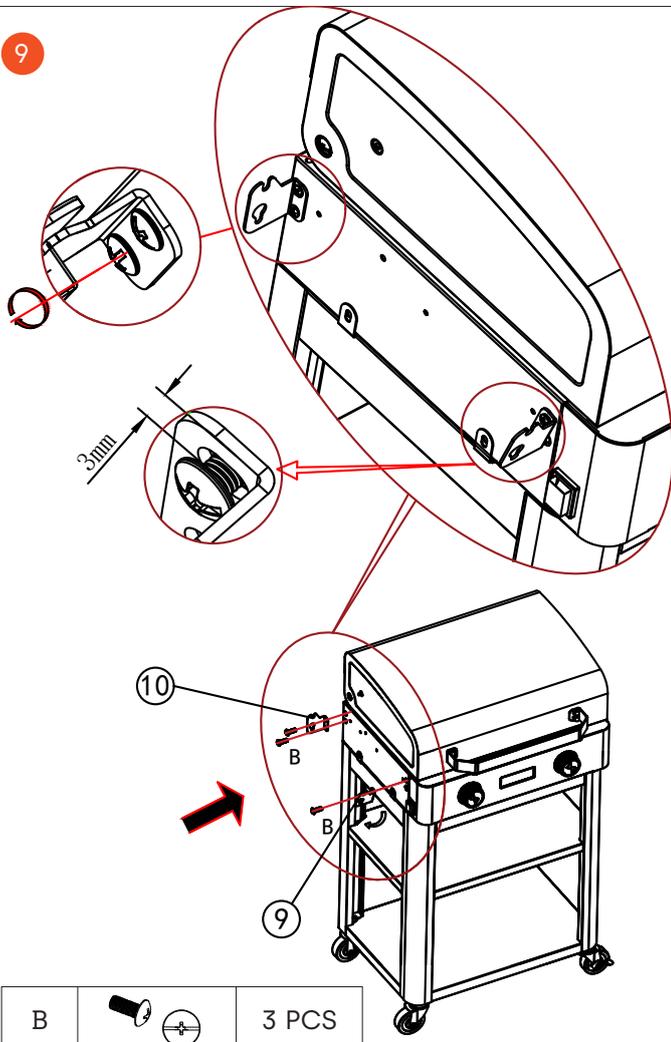
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A		6 PCS
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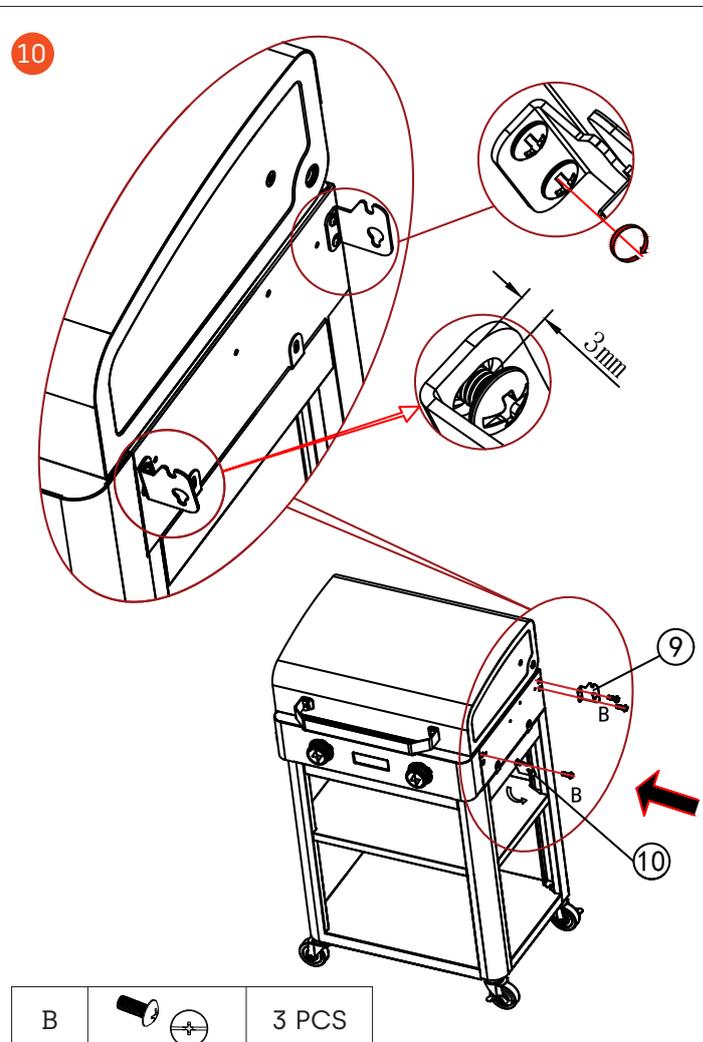
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9



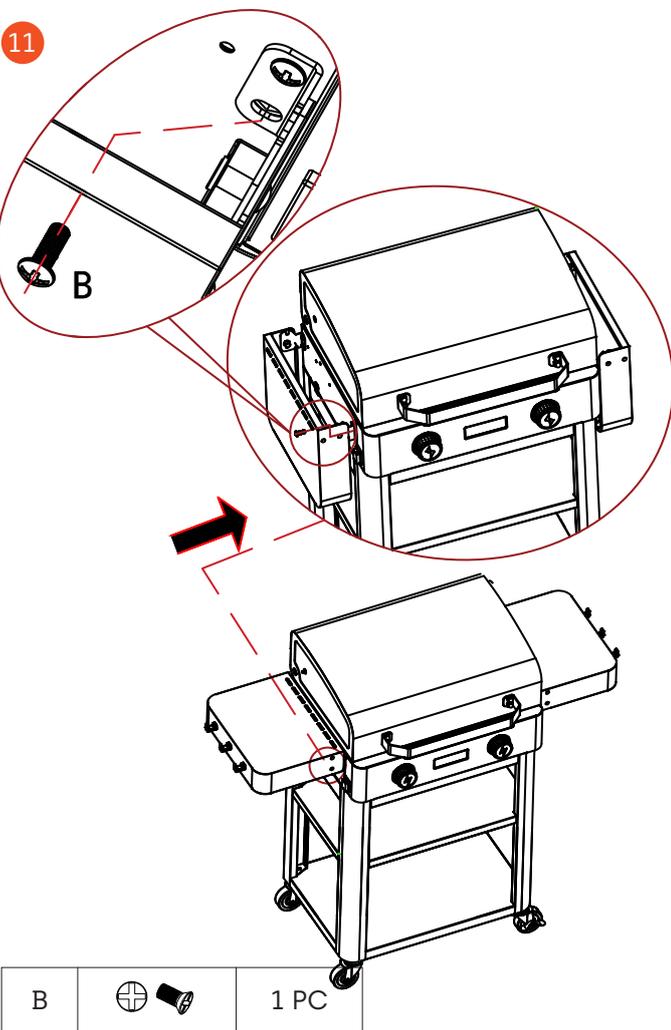
B		3 PCS
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10



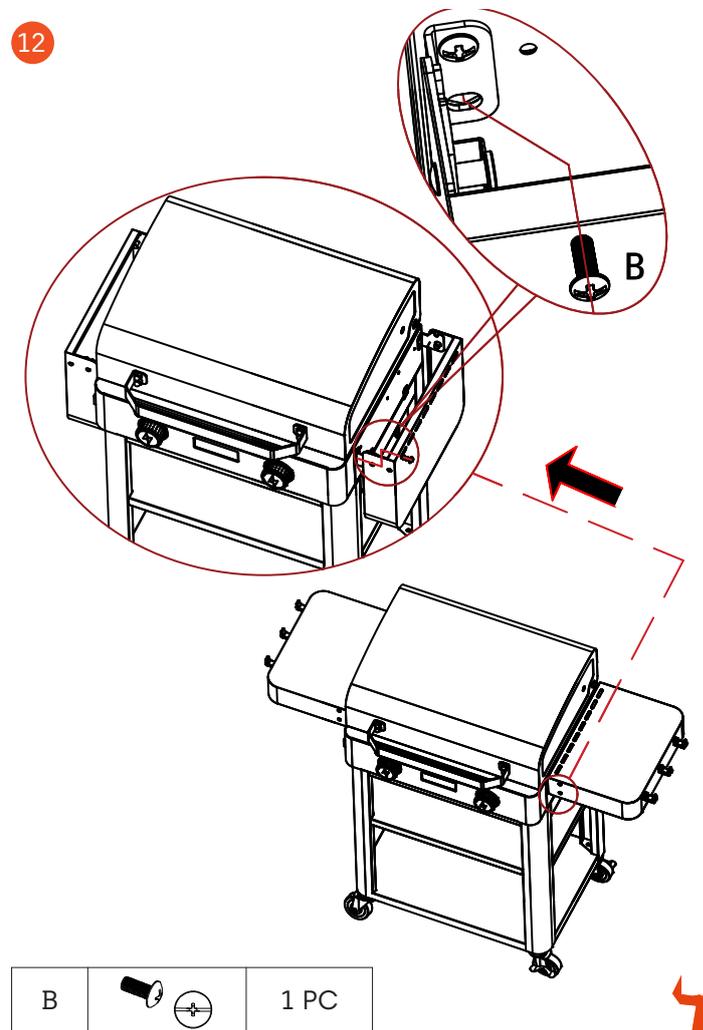
B		3 PCS
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11



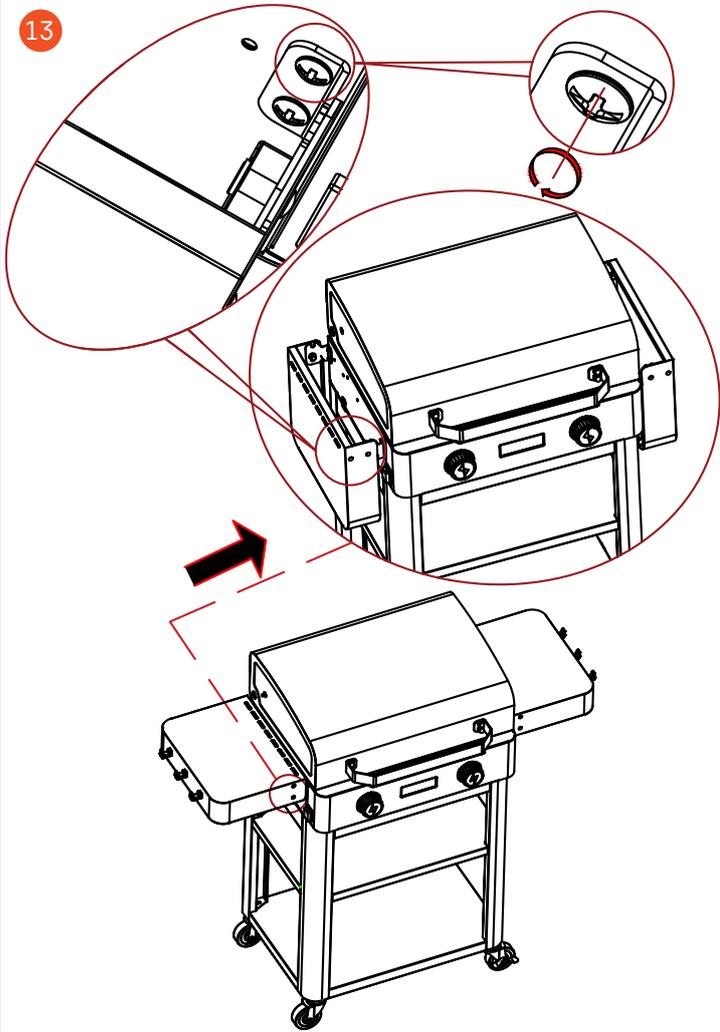
B		1 PC
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12

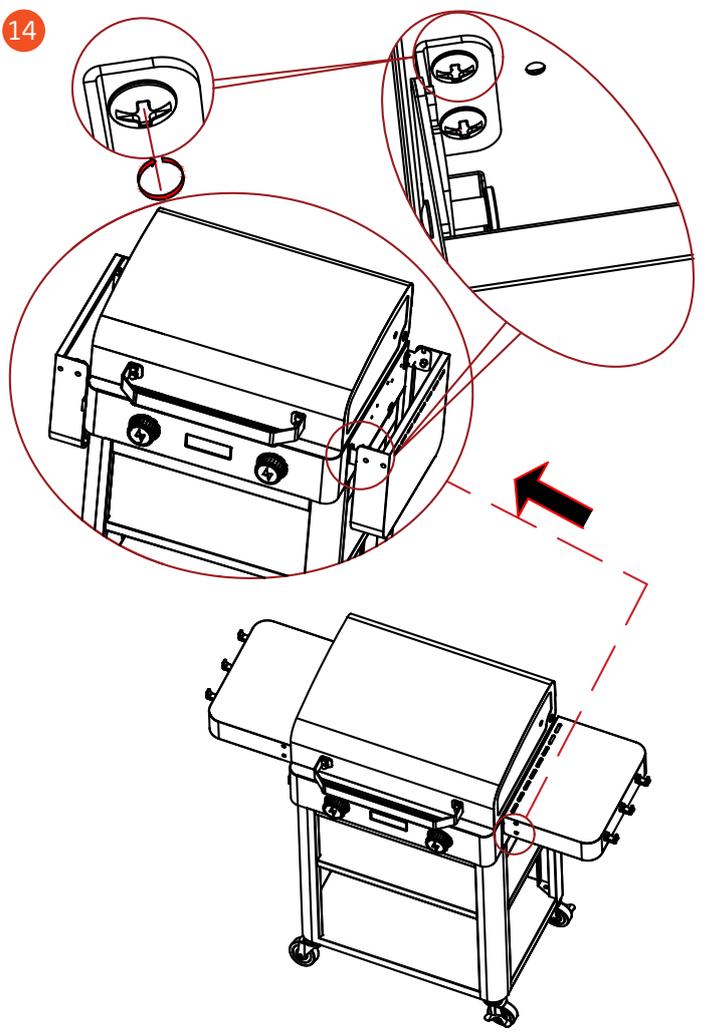


B		1 PC
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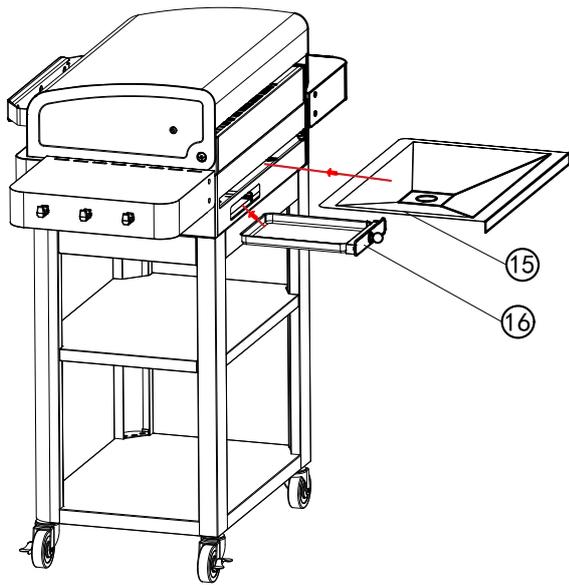
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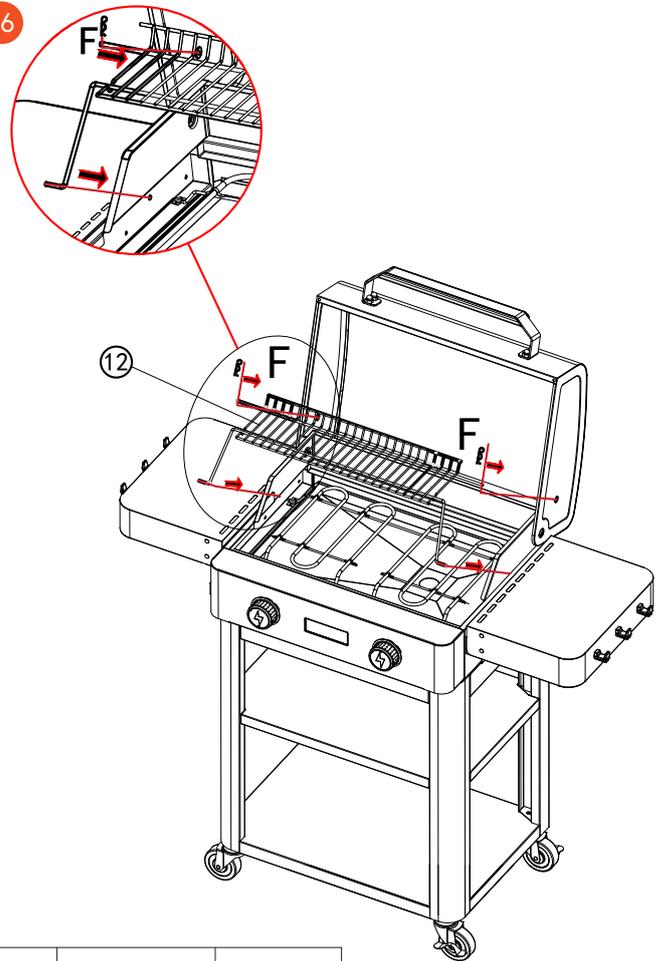
14



15



16



F



2 PCS

17

